

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE
UGC - AUTONOMOUS INSTITUTION
CIRCULAR

Date: 11.07.2025
Madanapalle

All the M. Tech I Year II Semester 2024 Batch (R24) Regular M.Tech students are hereby informed that the External Practical Exams will be conducted from 24.07.2025 to 25.07.2025 as per the time table given below.

M.Tech (VLSI & Embedded Systems) I Year II Semester (R24) Regular : 2024 Admitted Batch

CMOS Analog IC Design Laboratory 24VESP203

Date	Time	Roll No.S	No.of Students	Lab Slot
24.07.2025	09:30AM-12:30 PM	24691D6901-24691D6907, 24691D6909-24691D6915	14	EB-103

Embedded System Design Laboratory 24VESP204

Date	Time	Roll No.S	No.of Students	Lab Slot
25.07.2025	09:30AM-12:30 PM	24691D6901-24691D6907, 24691D6909-24691D6915	14	EB-103

Note : (1) **Detained & Unregistered** students of the current semester are not eligible to write the above examinations, if in case the examination is conducted for any such student, it stands cancelled without any prior intimation

(2) if any discrepancies are found, the same may be informed to the examination section


CONTROLLER OF EXAMINATIONS
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